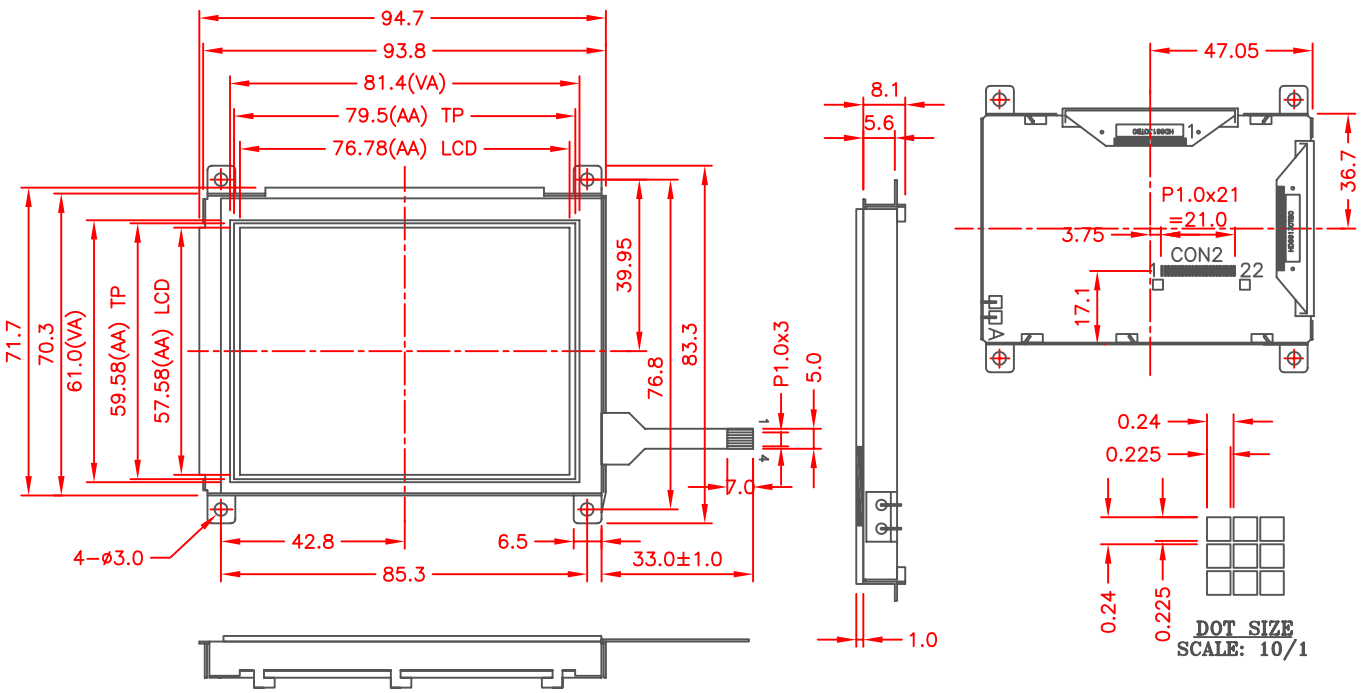
SHELLY 240x320 dots Graphic STG32024031



Feature

1.Built in SED1335 and SRAM/ SED 13700 ROW
2. Built-in positive voltage output
3. 1/240 duty cycle
4. Touch screen option

Pin Assignment

Pin	Symbol	Function
1	Vss	GND
2	Vdd	Power supply for logic
3	Vo	Operating voltage LCD driving
4	A0	H:Data L:Instruction
5	$\overline{WR}$	8080family: Read signal, 6800 family: Enable signal
6	$\overline{RD}$	8080family: Write signal, 6800 family: R/W signal
7	DB0	Data bus line
8	DB1	Data bus line
9	DB2	Data bus line
10	DB3	Data bus line
11	DB4	Data bus line
12	DB5	Data bus line
13	DB6	Data bus line
14	DB7	Data bus line
15	CS	Chip select
16	RES	Reset
17	VEE	Negative Voltage output
18	SEL1	8080 or 6800 interface select 1:68 0:80
19	A	Power supply for B/L
20	K	Power supply for B/L
21	NC	
22	NC	

Mechanical Data

Item	Standard Value	Unit
Module Dimension	94.7 x 71.7	mm
Viewing Area	81.4 x 61.0	mm
Dot Size	0.225 x 0.225	mm
Dot Pitch	0.24 x 0.24	mm
Mounting hole	85.3 x 83.3	mm

Absolute Maximum Rating

Item	Symbol	Standard Value			Unit
		min.	typ.	max.	
Power Supply	VDD-VSS	3.3	3.5	3.6	V
Input Voltage	VI	--	--	VDD	V

Note: VSS=0 Volt, VDD=5.0 Volt.

Electronical Characteristics

Item	Symbol	Condition	Standard Value			Unit
			min.	typ.	max.	
Input Voltage	VDD	L level	0	--	0.2VDD	V
	VIO	H level	0.5VDD	--	VDD	V
Supply Current	IDD	VDD=3.5	--	33	--	mA
Recommended LC Driving Voltage for Normal Temp. Version module	VDD-VO	-20°C	--	--	--	V
		0°C	--	--	--	
		25°C	--	25	--	
		50°C	--	--	--	
		70°C	--	--	--	
LED Forward Voltage	VF	25°C	--	25	--	V
LED Foward Current	IF	25°C	--			mA
CCFL	VF	25°C	--	--	--	Vm
	IF	25°C	--	--	--	mA
EL	IEL	Vel=110VAC;400Hz	--	--	--	mA

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